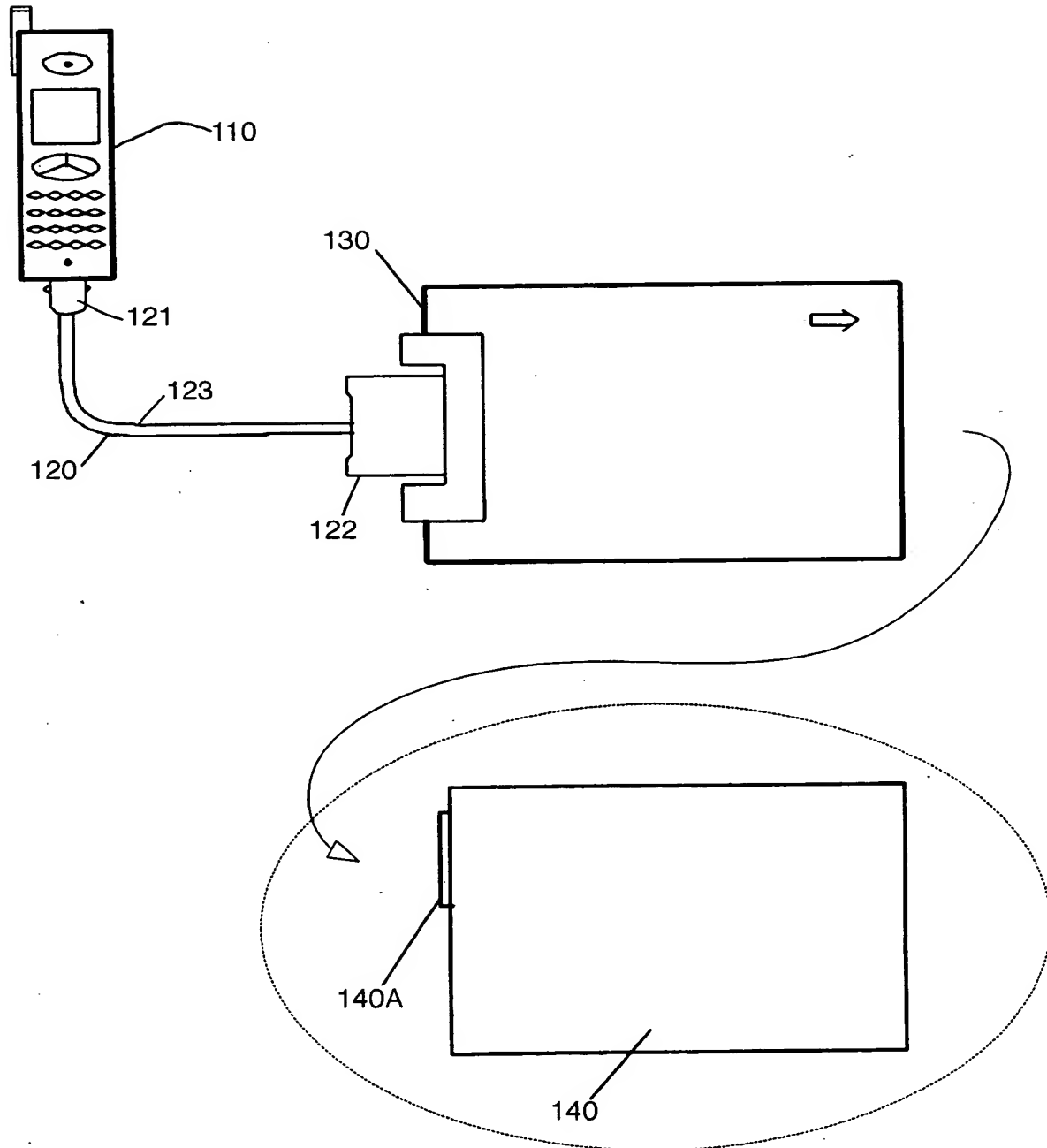


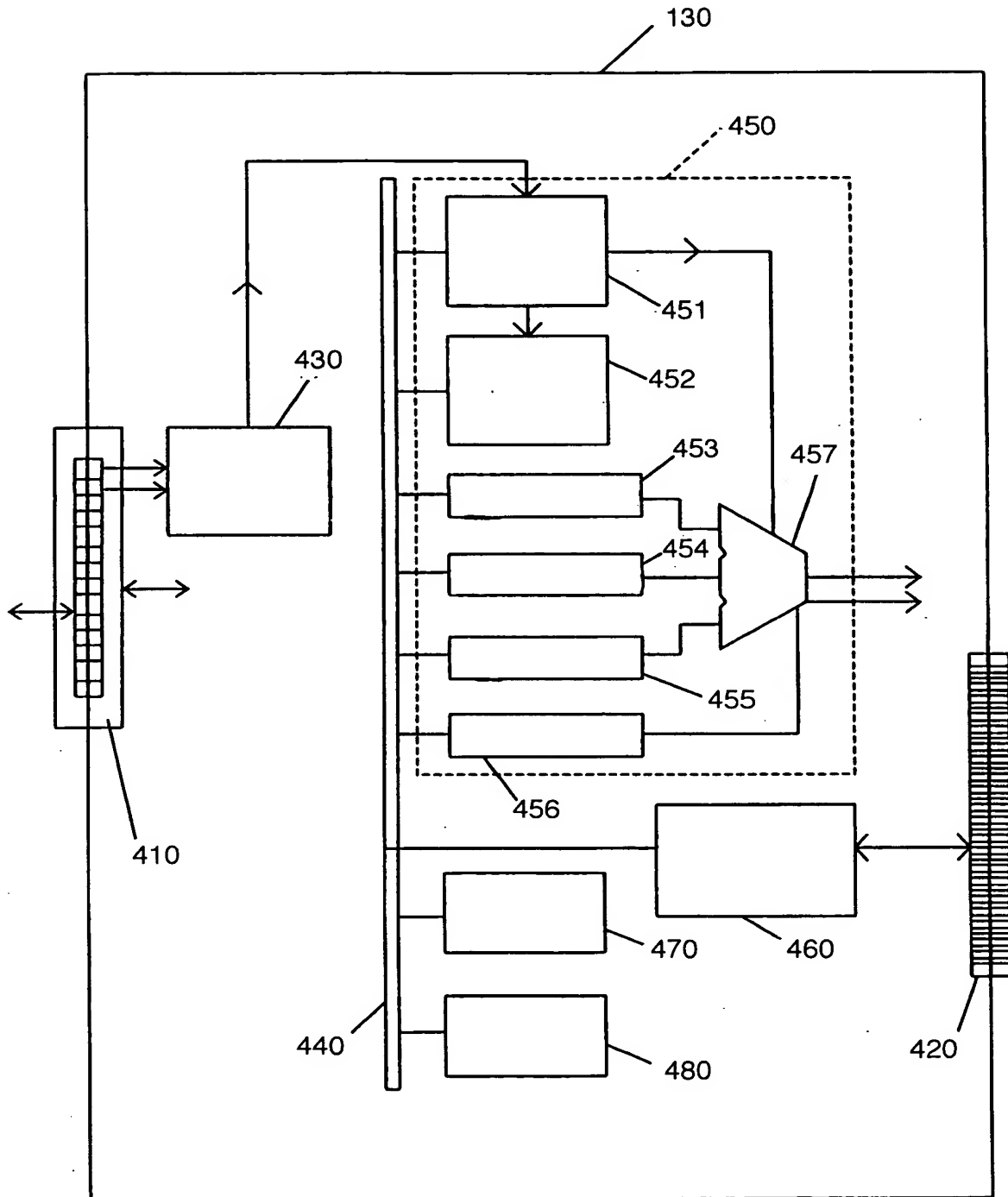
FIG. 1



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Diagram illustrating a cable assembly 120. The assembly includes a connector 121, a cable 123, and a plug 300. The plug 300 is shown inserted into a port 310 of a device 300A. A detail view 300B shows the internal contacts of the plug 300.

FIG. 4



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FIG. 5

Communication System	PDC	PHS Guarantee	PHS Best Effort
Communication Protocol	WORM-ARQ	PIAFS 2.0	PIAFS 2.1
Hardware Interface Specification	Frame Signal Synchronization Half-Duplex Transmission External Clock	Frame Signal Synchronization Full-Duplex Transmission External Clock	Frame Signal Synchronization (switching to 4bit/8bit) Full-Duplex Transmission External Clock
Dedicated Connector	16 Pins	18 Pins	12 Pins

COMMUNICATION SYSTEMS EMPLOYED BY A CELLULAR PHONE

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